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SystemVerilog For Design - Stuart Sutherland
2013-12-01

SystemVerilog is a rich set of extensions to the IEEE 1364-2001 Verilog Hardware Description Language (Verilog HDL). These extensions

address two major aspects of HDL based design. First, modeling very large designs with concise, accurate, and intuitive code. Second, writing high-level test programs to efficiently and effectively verify these large designs. This book,

SystemVerilog for Design, addresses the first aspect of the SystemVerilog extensions to Verilog. Important modeling features are presented, such as two-state data types, enumerated types, user-defined types, structures, unions, and interfaces. Emphasis is placed on the proper usage of these enhancements for simulation and synthesis. A companion to this book, SystemVerilog for Verification, covers the second aspect of SystemVerilog.

Monte Carlo - George Fishman 2013-03-09
Apart from a thorough exploration of all the important concepts, this volume includes over 75 algorithms, ready for putting into practice. The book also contains numerous hands-on implementations of selected algorithms to demonstrate applications in realistic settings. Readers are assumed to have a sound understanding of calculus, introductory matrix analysis, and intermediate statistics, but otherwise the book is self-contained. Suitable for

graduates and undergraduates in mathematics and engineering, in particular operations research, statistics, and computer science.

CMOS Digital Integrated Circuits - Sung-Mo Kang 2002

The fourth edition of CMOS Digital Integrated Circuits: Analysis and Design continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS

transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low power design techniques, design for manufacturability and design for testability.

Integrated Analog-To-Digital and Digital-To-Analog Converters - Rudy J. van de Plassche 2012-12-06

Analog-to-digital (A/D) and digital-to-analog (D/A) converters provide the link between the analog world of transducers and the digital world of signal processing, computing and other digital data collection or data processing systems. Several types of converters have been designed, each using the best available technology at a given time for a given application. For example, high-performance bipolar and MOS technologies have resulted in the design of high-resolution or high-speed converters with applications in digital audio and video systems. In addition, high-speed bipolar

technologies enable conversion speeds to reach the gigaHertz range and thus have applications in HDTV and digital oscilloscopes. Integrated Analog-to-Digital and Digital-to-Analog Converters describes in depth the theory behind and the practical design of these circuits. It describes the different techniques to improve the accuracy in high-resolution A/D and D/A converters and also special techniques to reduce the number of elements in high-speed A/D converters by repetitive use of comparators. Integrated Analog-to-Digital and Digital-to-Analog Converters is the most comprehensive book available on the subject. Starting from the basic elements of theory necessary for a complete understanding of the design of A/D and D/A converters, this book describes the design of high-speed A/D converters, high-accuracy D/A and A/D converters, sample-and-hold amplifiers, voltage and current reference sources, noise-shaping coding and sigma-delta converters. Integrated Analog-to-Digital and Digital-to-

Analog Converters contains a comprehensive bibliography and index and also includes a complete set of problems. This book is ideal for use in an advanced course on the subject and is an essential reference for researchers and practicing engineers.

Data Sources - 2000

**Who's Who in Science and Engineering
2008-2009** - Marquis Who's Who 2007-12

**Using Artificial Neural Networks for Analog
Integrated Circuit Design Automation** - João
P. S. Rosa 2019-12-11

This book addresses the automatic sizing and layout of analog integrated circuits (ICs) using deep learning (DL) and artificial neural networks (ANN). It explores an innovative approach to automatic circuit sizing where ANNs learn patterns from previously optimized design solutions. In opposition to classical optimization-based sizing strategies, where computational

intelligence techniques are used to iterate over the map from devices' sizes to circuits' performances provided by design equations or circuit simulations, ANNs are shown to be capable of solving analog IC sizing as a direct map from specifications to the devices' sizes. Two separate ANN architectures are proposed: a Regression-only model and a Classification and Regression model. The goal of the Regression-only model is to learn design patterns from the studied circuits, using circuit's performances as input features and devices' sizes as target outputs. This model can size a circuit given its specifications for a single topology. The Classification and Regression model has the same capabilities of the previous model, but it can also select the most appropriate circuit topology and its respective sizing given the target specification. The proposed methodology was implemented and tested on two analog circuit topologies.

Systematic Design of Analog CMOS Circuits -

Paul G. A. Jespers 2017-10-12

Discover a fresh approach to efficient and insight-driven analog integrated circuit design in nanoscale-CMOS with this hands-on guide.

Expert authors present a sizing methodology that employs SPICE-generated lookup tables, enabling close agreement between hand analysis and simulation. This enables the exploration of analog circuit tradeoffs using the gm/ID ratio as a central variable in script-based design flows, and eliminates time-consuming iterations in a circuit simulator. Supported by downloadable MATLAB code, and including over forty detailed worked examples, this book will provide professional analog circuit designers, researchers, and graduate students with the theoretical know-how and practical tools needed to acquire a systematic and re-use oriented design style for analog integrated circuits in modern CMOS.

CMOS analog circuit design - Allen Philip & Holberg Doug 2010

Silicon Photonics Design - Lukas Chrostowski 2015-03-12

This hands-on introduction to silicon photonics engineering equips students with everything they need to begin creating foundry-ready designs.

CMOS Analog Circuit Design - Behzad Razavi 2005

CMOS Analog Circuit Design, **CMOS Analog Circuit Design**, **MOS Analog Circuit Design**.

Fabless - Daniel Nenni 2014

The purpose of this book is to illustrate the magnificence of the fabless semiconductor ecosystem, and to give credit where credit is due. We trace the history of the semiconductor industry from both a technical and business perspective. We argue that the development of the fabless business model was a key enabler of the growth in semiconductors since the mid-1980s. Because business models, as much as the technology, are what keep us thrilled with new gadgets year after year, we focus on the

evolution of the electronics business. We also invited key players in the industry to contribute chapters. These "In Their Own Words" chapters allow the heavyweights of the industry to tell their corporate history for themselves, focusing on the industry developments (both in technology and business models) that made them successful, and how they in turn drive the further evolution of the semiconductor industry.

VLSI Design Techniques for Analog and Digital Circuits - Randall L. Geiger 1990

Modeling and Simulation for RF System Design - Ronny Frevert 2006-06-28

Modern telecommunication systems are highly complex from an algorithmic point of view. The complexity continues to increase due to advanced modulation schemes, multiple protocols and standards, as well as additional functionality such as personal organizers or navigation aids. To have short and reliable design cycles, efficient verification methods and

tools are necessary. Modeling and simulation need to accompany the design steps from the specification to the overall system verification in order to bridge the gaps between system specification, system simulation, and circuit level simulation. Very high carrier frequencies together with long observation periods result in extremely large computation times and requires, therefore, specialized modeling methods and simulation tools on all design levels. The focus of Modeling and Simulation for RF System Design lies on RF specific modeling and simulation methods and the consideration of system and circuit level descriptions. It contains application-oriented training material for RF designers which combines the presentation of a mixed-signal design flow, an introduction into the powerful standardized hardware description languages VHDL-AMS and Verilog-A, and the application of commercially available simulators. Modeling and Simulation for RF System Design is addressed to graduate students and industrial

professionals who are engaged in communication system design and want to gain insight into the system structure by own simulation experiences. The authors are experts in design, modeling and simulation of communication systems engaged at the Nokia Research Center (Bochum, Germany) and the Fraunhofer Institute for Integrated Circuits, Branch Lab Design Automation (Dresden, Germany).

Digital Integrated Circuit Design - Hubert Kaeslin 2008-04-28

This practical, tool-independent guide to designing digital circuits takes a unique, top-down approach, reflecting the nature of the design process in industry. Starting with architecture design, the book comprehensively explains the why and how of digital circuit design, using the physics designers need to know, and no more.

Physical Design Essentials - Khosrow Golshan 2007-04-08

Arranged in a format that follows the industry-common ASIC physical design flow, *Physical Design Essentials* begins with general concepts of an ASIC library, then examines floorplanning, placement, routing, verification, and finally, testing. Among the topics covered are Basic standard cell design, transistor-sizing, and layout styles; Linear, non-linear, and polynomial characterization; Physical design constraints and floorplanning styles; Algorithms used for placement; Clock Tree Synthesis; Parasitic extraction; Electronic Testing, and many more.

The gm/ID Methodology, a sizing tool for low-voltage analog CMOS Circuits - Paul Jespers 2009-12-01

IC designers appraise currently MOS transistor geometries and currents to compromise objectives like gain-bandwidth, slew-rate, dynamic range, noise, non-linear distortion, etc. Making optimal choices is a difficult task. How to minimize for instance the power consumption of an operational amplifier without too much

penalty regarding area while keeping the gain-bandwidth unaffected in the same time?
Moderate inversion yields high gains, but the concomitant area increase adds parasitics that restrict bandwidth. Which methodology to use in order to come across the best compromise(s)? Is synthesis a mixture of design experience combined with cut and tries or is it a constrained multivariate optimization problem, or a mixture? Optimization algorithms are attractive from a system perspective of course, but what about low-voltage low-power circuits, requiring a more physical approach? The connections amid transistor physics and circuits are intricate and their interactions not always easy to describe in terms of existing software packages. The gm/ID synthesis methodology is adapted to CMOS analog circuits for the transconductance over drain current ratio combines most of the ingredients needed in order to determine transistors sizes and DC currents.

Education and Educational Technology -

Yuanzhi Wang 2011-10-07

This volume includes extended and revised versions of a set of selected papers from the 2011 2nd International Conference on Education and Educational Technology (EET 2011) held in Chengdu, China, October 1-2, 2011. The mission of EET 2011 Volume 1 is to provide a forum for researchers, educators, engineers, and government officials involved in the general areas of education and educational technology to disseminate their latest research results and exchange views on the future research directions of these fields. 130 related topic papers were selected into this volume. All the papers were reviewed by 2 program committee members and selected by the volume editor Prof. Yuanzhi Wang, from Intelligent Information Technology Application Research Association, Hong Kong. The conference will bring together leading researchers, engineers and scientists in the domain of interest. We hope every participant can have a good opportunity to

exchange their research ideas and results and to discuss the state of the art in the areas of the education and educational technology.

Smart Systems Integration and Simulation -

Nicola Bombieri 2016-02-17

This book-presents new methods and tools for the integration and simulation of smart devices. The design approach described in this book explicitly accounts for integration of Smart Systems components and subsystems as a specific constraint. It includes methodologies and EDA tools to enable multi-disciplinary and multi-scale modeling and design, simulation of multi-domain systems, subsystems and components at all levels of abstraction, system integration and exploration for optimization of functional and non-functional metrics. By covering theoretical and practical aspects of smart device design, this book targets people who are working and studying on hardware/software modelling, component integration and simulation under different

positions (system integrators, designers, developers, researchers, teachers, students etc.). In particular, it is a good introduction to people who have interest in managing heterogeneous components in an efficient and effective way on different domains and different abstraction levels. People active in smart device development can understand both the current status of practice and future research directions.

- Provides a comprehensive overview of smart systems design, focusing on design challenges and cutting-edge solutions;
- Enables development of a co-simulation and co-design environment that accounts for the peculiarities of the basic subsystems and components to be integrated;
- Describes development of modeling and design techniques, methods and tools that enable multi-domain simulation and optimization at various levels of abstraction and across different technological domains.

RF Circuit Design - Christopher Bowick

2014-06-28

Essential reading for experts in the field of RF circuit design and engineers needing a good reference. This book provides complete design procedures for multiple-pole Butterworth, Chebyshev, and Bessel filters. It also covers capacitors, inductors, and other components with their behavior at RF frequencies discussed in detail. Provides complete design procedures for multiple-pole Butterworth, Chebyshev, and Bessel filters Covers capacitors, inductors, and other components with their behavior at RF frequencies discussed in detail

Circadian Rhythms for Future Resilient

Electronic Systems - Xinfei Guo 2019-06-12

This book describes methods to address wearout/aging degradations in electronic chips and systems, caused by several physical mechanisms at the device level. The authors introduce a novel technique called accelerated active self-healing, which fixes wearout issues by enabling accelerated recovery. Coverage includes recovery theory, experimental results,

implementations and applications, across multiple nodes ranging from planar, FD-SOI to FinFET, based on both foundry provided models and predictive models. Presents novel techniques, tested with experiments on real hardware; Discusses circuit and system level wearout recovery implementations, many of these designs are portable and friendly to the standard design flow; Provides circuit-architecture-system infrastructures that enable the accelerated self-healing for future resilient systems; Discusses wearout issues at both transistor and interconnect level, providing solutions that apply to both; Includes coverage of resilient aspects of emerging applications such as IoT.

Analog Design Essentials - Willy M Sansen

2007-02-03

This unique book contains all topics of importance to the analog designer which are essential to obtain sufficient insights to do a thorough job. The book starts with elementary

stages in building up operational amplifiers. The synthesis of opamps is covered in great detail. Many examples are included, operating at low supply voltages. Chapters on noise, distortion, filters, ADC/DACs and oscillators follow. These are all based on the extensive amount of teaching that the author has carried out world-wide.

Digital VLSI Chip Design with Cadence and Synopsys CAD Tools - Erik Brunvand 2010

Digital VLSI Chip Design with Cadence and Synopsys CAD Tools leads students through the complete process of building a ready-to-fabricate CMOS integrated circuit using popular commercial design software. Detailed tutorials include step-by-step instructions and screen shots of tool windows and dialog boxes. This hands-on book is for use in conjunction with a primary textbook on digital VLSI. University instructors may order Digital VLSI Chip Design with Cadence and Synopsys CAD Tools with the following textbooks: [Rabaey Cover Image]

Digital Integrated Circuits, 2nd Edition, by Jan M. Rabaey, Anantha Chandrakasan, and Borivoje Nikoli. To order Digital Integrated Circuits, 2nd Edition packaged with Digital VLSI Chip Design with Cadence and Synopsys CAD Tools, please use ISBN 0-13-509470-4 on your bookstore order form. [Weste Cover Image] CMOS VLSI Design, 3rd Edition, by Neil H.E. Weste and David Harris. To order CMOS VLSI Design, 3rd Edition packaged with Digital VLSI Chip Design with Cadence and Synopsys CAD Tools, please use ISBN 0-13-509469-0 on your bookstore order form. For further details, please contact your local Pearson (Addison-Wesley and Prentice Hall) sales representative or visit www.pearsonhighered.com.

Layout Design and Verification - T. Ohtsuki 1986

This book covers algorithmic aspects of computer aided circuit design for VLSI of large circuits. The large scale aspect of VLSI requires a reorientation towards new and more efficient

techniques. Many algorithms have survived the test of time, while others are suffering from the usual problem of polynomial or exponential running time complexity and storage requirements. The approaches presented in this book are techniques which were developed in response to the VLSI problems. The most recent exact" circuit analysis and simulation techniques are presented, such as waveform relaxation and timing simulation. The book concentrates on the analysis and simulation of large circuits which exceed the capabilities of general purpose analyzers in both compute time and storage. Also discussed are circuit models for switch level simulation, techniques and circuit models for interconnections, capacitance and inductances and optimization techniques. The language and notation have been kept uniform throughout the book to help the reader to maintain the continuity between the topics discussed in the different chapters. All algorithms are written in a Pascal style. The terminology used should

reflect the emerging language used in most of the VLSI circuit design community. The book includes proven approaches as well as techniques which are presently in a research state.

Analog Circuits and Systems Optimization based on Evolutionary Computation Techniques - Manuel Barros 2010-04-22

The microelectronics market, with special emphasis to the production of complex mixed-signal systems-on-chip (SoC), is driven by three main dynamics, time-- market, productivity and managing complexity. Pushed by the progress in na- meter technology, the design teams are facing a curve of complexity that grows exponentially, thereby slowing down the productivity design rate. Analog design automation tools are not developing at the same pace of technology, once custom design, characterized by decisions taken at each step of the analog design flow, - lies most of the time on designer knowledge and expertise. Actually, the

use of - sign management platforms, like the Cadences Virtuoso platform, with a set of -tegrated CAD tools and database facilities to deal with the design transformations from the system level to the physical implementation, can significantly speed-up the design process and enhance the productivity of analog/mixed-signal integrated circuit (IC) design teams. These design management platforms are a valuable help in analog IC design but they are still far behind the development stage of design automation tools already available for digital design. Therefore, the development of new CAD tools and design methodologies for analog and mixed-signal ICs is essential to increase the designer's productivity and reduce design productivitygap. The work presented in this book describes a new design automation approach to the problem of sizing analog ICs. Intelligent System Design - Suresh Chandra Satapaty 2020-08-10

This book presents a collection of high-quality,

peer-reviewed research papers from the 6th International Conference on Information System Design and Intelligent Applications (INDIA 2019), held at Lendi Institute of Engineering & Technology, India, from 1 to 2 November 2019. It covers a wide range of topics in computer science and information technology, including data mining and data warehousing, high-performance computing, parallel and distributed computing, computational intelligence, soft computing, big data, cloud computing, grid computing and cognitive computing.

Artificial Intelligence and Evolutionary Computations in Engineering Systems - Subhransu Sekhar Dash 2016-02-05

The book is a collection of high-quality peer-reviewed research papers presented in the first International Conference on International Conference on Artificial Intelligence and Evolutionary Computations in Engineering Systems (ICAIECES -2015) held at Velammal Engineering College (VEC), Chennai, India

during 22 - 23 April 2015. The book discusses wide variety of industrial, engineering and scientific applications of the emerging techniques. Researchers from academic and industry present their original work and exchange ideas, information, techniques and applications in the field of Communication, Computing and Power Technologies.

Proceedings - 2001

CMOS - R. Jacob Baker 2008

This edition provides an important contemporary view of a wide range of analog/digital circuit blocks, the BSIM model, data converter architectures, and more. The authors develop design techniques for both long- and short-channel CMOS technologies and then compare the two.

Analogue IC Design - Chris Toumazou 1993

Analogue IC Design has become the essential title covering the current-mode approach to integrated circuit design. The approach has

sparked much interest in analogue electronics and is linked to important advances in integrated circuit technology, such as CMOS VLSI which allows mixed analogue and digital circuits and high-speed GaAs processing.

IEEE 2000 First International Symposium on Quality Electronic Design - 2000

Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation - Nadine Azemard 2007-08-21

This volume features the refereed proceedings of the 17th International Workshop on Power and Timing Modeling, Optimization and Simulation. Papers cover high level design, low power design techniques, low power analog circuits, statistical static timing analysis, power modeling and optimization, low power routing optimization, security and asynchronous design, low power applications, modeling and optimization, and more.

High-Frequency Integrated Circuits - Sorin

Voinigescu 2013-02-28

A transistor-level, design-intensive overview of high speed and high frequency monolithic integrated circuits for wireless and broadband systems from 2 GHz to 200 GHz, this comprehensive text covers high-speed, RF, mm-wave, and optical fibre circuits using nanoscale CMOS, SiGe BiCMOS, and III-V technologies. Step-by-step design methodologies, end-of chapter problems, and practical simulation and design projects are provided, making this an ideal resource for senior undergraduate and graduate courses in circuit design. With an emphasis on device-circuit topology interaction and optimization, it gives circuit designers and students alike an in-depth understanding of device structures and process limitations affecting circuit performance.

Extreme Environment Electronics - John D.

Cressler 2017-12-19

Unfriendly to conventional electronic devices, circuits, and systems, extreme environments

represent a serious challenge to designers and mission architects. The first truly comprehensive guide to this specialized field, Extreme Environment Electronics explains the essential aspects of designing and using devices, circuits, and electronic systems intended to operate in extreme environments, including across wide temperature ranges and in radiation-intense scenarios such as space. The Definitive Guide to Extreme Environment Electronics Featuring contributions by some of the world's foremost experts in extreme environment electronics, the book provides in-depth information on a wide array of topics. It begins by describing the extreme conditions and then delves into a description of suitable semiconductor technologies and the modeling of devices within those technologies. It also discusses reliability issues and failure mechanisms that readers need to be aware of, as well as best practices for the design of these electronics. Continuing beyond just the "paper design" of building blocks, the

book rounds out coverage of the design realization process with verification techniques and chapters on electronic packaging for extreme environments. The final set of chapters describes actual chip-level designs for applications in energy and space exploration. Requiring only a basic background in electronics, the book combines theoretical and practical aspects in each self-contained chapter. Appendices supply additional background material. With its broad coverage and depth, and the expertise of the contributing authors, this is an invaluable reference for engineers, scientists, and technical managers, as well as researchers and graduate students. A hands-on resource, it explores what is required to successfully operate electronics in the most demanding conditions.

The Art of Analog Layout - Alan Hastings 2006
For Electrical Engineering courses in analog layout or professional layout designers. This text covers the issues involved in successfully laying out analog integrated circuits. Hastings provides

clear guidance and does not stress theoretical physics or mathematical analysis of layouts. He emphasizes cross- sections of devices and carrier-based models of device operation as compared to the more common geometric and schematic representation of devices.

Extreme Environment Electronics - John D. Cressler 2017-12-19

Unfriendly to conventional electronic devices, circuits, and systems, extreme environments represent a serious challenge to designers and mission architects. The first truly comprehensive guide to this specialized field, *Extreme Environment Electronics* explains the essential aspects of designing and using devices, circuits, and electronic systems intended to operate in extreme environments, including across wide temperature ranges and in radiation-intense scenarios such as space. *The Definitive Guide to Extreme Environment Electronics* Featuring contributions by some of the world's foremost experts in extreme environment electronics, the

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an invaluable reference for engineers, scientists, and technical managers, as well as researchers and graduate students. A hands-on resource, it explores what is required to successfully operate electronics in the most demanding conditions.

Using the Electric VLSI Design System - Steven M. Rubin 2009-02

Multi-voltage CMOS Circuit Design - Volkan Kursun 2006-08-30

This book presents an in-depth treatment of various power reduction and speed enhancement techniques based on multiple supply and threshold voltages. A detailed discussion of the sources of power consumption in CMOS circuits will be provided whilst focusing primarily on identifying the mechanisms by which sub-threshold and gate oxide leakage currents are generated. The authors present a comprehensive review of state-of-the-art dynamic, static supply and threshold voltage scaling techniques and discuss

the pros and cons of supply and threshold voltage scaling techniques.

A Verilog HDL Primer - Jayaram Bhasker
2005-01-01

Advances in VLSI, Communication, and Signal Processing - David Harvey 2020-10-14

This book comprises select peer-reviewed papers from the International Conference on VLSI, Communication and Signal processing (VCAS) 2019, held at Motilal Nehru National Institute of Technology (MNNIT) Allahabad, Prayagraj,

India. The contents focus on latest research in different domains of electronics and communication engineering, in particular microelectronics and VLSI design, communication systems and networks, and signal and image processing. The book also discusses the emerging applications of novel tools and techniques in image, video and multimedia signal processing. This book will be useful to students, researchers and professionals working in the electronics and communication domain.