

Intel Nand Flash Memory

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[A History of Communication Technology](#) - Philip Loubere
2021-04-12

This book is a comprehensive illustrated account of the technologies and inventions in mass communication that have accelerated the advancement of human culture and society. A History of Communication Technology covers a timeline in the history of mass communication that begins with human prehistory and extends all the way to the current digital age. Using rich, full-color graphics and diagrams, the book details the workings of various mass communication inventions, from paper-making, printing presses, photography, radio, TV, film, and video, to computers, digital devices, and the Internet. Readers are given insightful narratives on the social impact of these technologies, brief historical accounts of the inventors, and sidebars on the related technologies that enabled these inventions. This book is ideal for students in introductory mass communication, visual communication, and history of media courses, offering a highly approachable, graphic-oriented approach to the history of communication technologies.

PC Mag - 2007-07-17

PCMag.com is a leading authority on technology, delivering Labs-based, independent reviews of the latest products and services.

Our expert industry analysis and practical solutions help you make better buying decisions and get more from technology.

Flash Memories - Detlev Richter 2013-09-12

The subject of this book is to introduce a model-based quantitative performance indicator methodology applicable for performance, cost and reliability optimization of non-volatile memories. The complex example of flash memories is used to introduce and apply the methodology. It has been developed by the author based on an industrial 2-bit to 4-bit per cell flash development project. For the first time, design and cost aspects of 3D integration of flash memory are treated in this book. Cell, array, performance and reliability effects of flash memories are introduced and analyzed. Key performance parameters are derived to handle the flash complexity. A performance and array memory model is developed and a set of performance indicators characterizing architecture, cost and durability is defined. Flash memories are selected to apply the Performance Indicator Methodology to quantify design and technology innovation. A graphical representation based on trend lines is introduced to support a requirement based product development process. The Performance Indicator methodology is applied to demonstrate the importance of hidden memory parameters for a successful

product and system development roadmap. Flash Memories offers an opportunity to enhance your understanding of product development key topics such as:

- Reliability optimization of flash memories is all about threshold voltage margin understanding and definition;
- Product performance parameter are analyzed in-depth in all aspects in relation to the threshold voltage operation window;
- Technical characteristics are translated into quantitative performance indicators;
- Performance indicators are applied to identify and quantify product and technology innovation within adjacent areas to fulfill the application requirements with an overall cost optimized solution;
- Cost, density, performance and durability values are combined into a common factor - performance indicator - which fulfills the application requirements

Embedded Software and Systems - Laurence T. Yang
2005-11-30

Welcome to the proceedings of the 2005 International Conference on Embedded Software and Systems (ICISS 2005) held in Xian, China, December 16-18, 2005. With the advent of VLSI system level integration and system-on-chip, the center of gravity of the computer industry is now moving from personal computing into embedded computing. Embedded software and systems are increasingly becoming a key technological component of all kinds of complex technical systems, ranging from vehicles, telephones, aircraft, toys, security systems, to medical diagnostics, weapons, pacemakers, climate control systems, etc. The ICISS 2005 conference provided a premier international forum for researchers, developers and providers from academia and industry to address all resulting profound challenges; to present and discuss their new ideas, search results, applications and experience; to improve international communication and cooperation; and to promote embedded software and system industrialization and wide applications on all aspects of embedded software and systems.

Guide to State-of-the-Art Electron Devices - Joachim N. Burghartz 2013-03-19

Winner, 2013 PROSE Award, Engineering and Technology Concise, high quality and comparative overview of state-of-the-art electron device development, manufacturing technologies and applications Guide to State-of-the-Art Electron Devices marks the 60th anniversary of the IRE electron devices committee and the 35th anniversary of the IEEE Electron Devices Society, as such it defines the state-of-the-art of electron devices, as well as future directions across the entire field. Spans full range of electron device types such as photovoltaic devices, semiconductor manufacturing and VLSI technology and circuits, covered by IEEE Electron and Devices Society Contributed by internationally respected members of the electron devices community A timely desk reference with fully-integrated colour and a unique lay-out with sidebars to highlight the key terms Discusses the historical developments and speculates on future trends to give a more rounded picture of the topics covered A valuable resource R&D managers; engineers in the semiconductor industry; applied scientists; circuit designers; Masters students in power electronics; and members of the IEEE Electron Device Society.

Advances in Non-volatile Memory and Storage Technology - Yoshio Nishi 2019-06-15

Advances in Nonvolatile Memory and Storage Technology, Second Edition, addresses recent developments in the non-volatile memory spectrum, from fundamental understanding, to technological aspects. The book provides up-to-date information on the current memory technologies as related by leading experts in both academia and industry. To reflect the rapidly changing field, many new chapters have been included to feature the latest in RRAM technology, STT-RAM, memristors and more. The new edition describes the emerging technologies including oxide-based ferroelectric memories, MRAM technologies, and 3D memory. Finally, to further widen the discussion on the

applications space, neuromorphic computing aspects have been included. This book is a key resource for postgraduate students and academic researchers in physics, materials science and electrical engineering. In addition, it will be a valuable tool for research and development managers concerned with electronics, semiconductors, nanotechnology, solid-state memories, magnetic materials, organic materials and portable electronic devices.

Discusses emerging devices and research trends, such as neuromorphic computing and oxide-based ferroelectric memories Provides an overview on developing nonvolatile memory and storage technologies and explores their strengths and weaknesses Examines improvements to flash technology, charge trapping and resistive random access memory

Samsung Electronics and the Struggle for Leadership of the Electronics Industry - Anthony Michell 2011-09-29

This book views Samsung Electronics in terms of corporate life cycle as well as product portfolio and strategy. It also examines the issues Samsung faces as it proceeds further into the 21st century. Written from the perspective of an experienced commentator on Korean and global business, this book presents not simply a narrative or an adulatory and uncritical account of Samsung's rise, but a considered analysis of the secrets of success that both business students and CEOs will want to read and consider applying to their own companies.

Strategic Management - Jeffrey H. Dyer 2020-01-29

Strategic Management delivers an insightful, clear, concise introduction to strategy management concepts and links these concepts to the skills and knowledge students need to be successful in the professional world. Written in a conversational Harvard Business Review style, this product sparks ideas, fuels creative thinking and discussion, while engaging students via contemporary examples, innovative whiteboard animations for each chapter, outstanding author-produced cases, unique Strategy Tool Applications with accompanying animations and

Career Readiness applications through author videos.

Computational Science And Its Applications - Iccsa 2005 -

Oswaldo Gervasi 2005-04-27

The four-volume set LNCS 3480-3483 constitutes the refereed proceedings of the International Conference on Computational Science and Its Applications, ICCSA 2005, held in Singapore in May 2005. The four volumes present a total of 540 papers selected from around 2700 submissions. The papers span the whole range of computational science, comprising advanced applications in virtually all sciences making use of computational techniques as well as foundations, techniques, and methodologies from computer science and mathematics, such as high performance computing and communication, networking, optimization, information systems and technologies, scientific visualization, graphics, image processing, data analysis, simulation and modelling, software systems, algorithms, security, multimedia etc.

Electronic Business - 2006

The management magazine for the electronics industry.

Mobile Terminal Receiver Design - Sajal Kumar Das 2017-05-01

MOBILE TERMINAL RECEIVER DESIGN MOBILE TERMINAL RECEIVER DESIGN LTE and LTE-Advanced India This all-in-one guide addresses the challenges of designing innovative mobile handset solutions that offer smaller size, low power consumption, low cost, and tremendous flexibility, with improved data rates and higher performance. Readers are introduced to mobile phone system architecture and its basic building blocks, different air interface standards and operating principles, before progressing to hardware anatomy, software and protocols, and circuits for legacy and next-generation smart phones, including various research areas in 4G and 5G systems. Mobile Terminal Receiver Design/p? ulliexplains basic working principles, system architecture and specification detailsof legacy and possible next-generation mobile systems, from principle to practiceto product;

covers in detail RF transmitter and receiver blocks, digital baseband processing blocks, receiver and transmitter signal processing, protocol stack, AGC, AFC, ATC, power supply, clocking; features important topics like connectivity and application modules with different design solutions for tradeoff exploration; discusses multi-RAT design requirements, key design attributes such as low power consumption, slim form factors, seamless I-RAT handover, sensitivity, and selectivity. It will help software, hardware, and radio frequency design engineers to understand the evolution of radio access technologies and to design competitive and innovative mobile solutions and devices. Graduates, postgraduate students, and researchers in mobile telecommunications disciplines will also find this book a handy reference.

Storage Systems - Alexander Thomasian 2021-10-13

Storage Systems: Organization, Performance, Coding, Reliability and Their Data Processing was motivated by the 1988 Redundant Array of Inexpensive/Independent Disks proposal to replace large form factor mainframe disks with an array of commodity disks. Disk loads are balanced by striping data into strips—with one strip per disk—and storage reliability is enhanced via replication or erasure coding, which at best dedicates k strips per stripe to tolerate k disk failures. Flash memories have resulted in a paradigm shift with Solid State Drives (SSDs) replacing Hard Disk Drives (HDDs) for high performance applications. RAID and Flash have resulted in the emergence of new storage companies, namely EMC, NetApp, SanDisk, and Purestorage, and a multibillion-dollar storage market. Key new conferences and publications are reviewed in this book. The goal of the book is to expose students, researchers, and IT professionals to the more important developments in storage systems, while covering the evolution of storage technologies, traditional and novel databases, and novel sources of data. We describe several prototypes: FAWN at CMU, RAMCloud at Stanford, and

Lightstore at MIT; Oracle's Exadata, AWS' Aurora, Alibaba's PolarDB, Fungible Data Center; and author's paper designs for cloud storage, namely heterogeneous disk arrays and hierarchical RAID. • Surveys storage technologies and lists sources of data: measurements, text, audio, images, and video • Familiarizes with paradigms to improve performance: caching, prefetching, log-structured file systems, and merge-trees (LSMs) • Describes RAID organizations and analyzes their performance and reliability • Conserves storage via data compression, deduplication, compaction, and secures data via encryption • Specifies implications of storage technologies on performance and power consumption • Exemplifies database parallelism for big data, analytics, deep learning via multicore CPUs, GPUs, FPGAs, and ASICs, e.g., Google's Tensor Processing Units

Plunkett's Engineering & Research Industry Almanac 2008

- Jack W. Plunkett 2008-05

A guide to the trends and leading companies in the engineering, research, design, innovation and development business fields: those firms that are dominant in engineering-based design and development, as well leaders in technology-based research and development.

Flash Memory Integration - Jalil Boukhobza 2017-03-10

4 zettabytes (4 billion terabytes) of data generated in 2013, 44 zettabytes predicted for 2020 and 185 zettabytes for 2025. These figures are staggering and perfectly illustrate this new era of data deluge. Data has become a major economic and social challenge. The speed of processing of these data is the weakest link in a computer system: the storage system. It is therefore crucial to optimize this operation. During the last decade, storage systems have experienced a major revolution: the advent of flash memory. *Flash Memory Integration: Performance and Energy Issues* contributes to a better understanding of these revolutions. The authors offer us an insight into the integration of flash memory in computer systems, their behavior in performance and in power

consumption compared to traditional storage systems. The book also presents, in their entirety, various methods for measuring the performance and energy consumption of storage systems for embedded as well as desktop/server computer systems. We are invited on a journey to the memories of the future. Ideal for computer scientists, featuring low level details to concentrate on system issues Tackles flash memory aspects while spanning domains such as embedded systems and HPC Contains an exhaustive set of experimental results conducted in the Lab-STICC laboratory Provides details on methodologies to perform performance and energy measurements on flash storage systems 3D IC and RF SiPs: Advanced Stacking and Planar Solutions for 5G Mobility - Lih-Tyng Hwang 2018-03-28

An interdisciplinary guide to enabling technologies for 3D ICs and 5G mobility, covering packaging, design to product life and reliability assessments Features an interdisciplinary approach to the enabling technologies and hardware for 3D ICs and 5G mobility Presents statistical treatments and examples with tools that are easily accessible, such as Microsoft's Excel and Minitab Fundamental design topics such as electromagnetic design for logic and RF/passives centric circuits are explained in detail Provides chapter-wise review questions and powerpoint slides as teaching tools

Digital Storage in Consumer Electronics - Thomas M. Coughlin 2017-12-09

This book provides an introduction to digital storage for consumer electronics. It discusses the various types of digital storage, including emerging non-volatile solid-state storage technologies and their advantages and disadvantages. It discusses the best practices for selecting, integrating, and using storage devices for various applications. It explores the networking of devices into an overall organization that results in always-available home storage combined with digital storage in the cloud to create an infrastructure to support emerging

consumer applications and the Internet of Things. It also looks at the role of digital storage devices in creating security and privacy in consumer products.

Cracking the Innovation Code - Andy Wynn 2020-11-02

Author Dr Andy Wynn, along with contributions from leaders of some of the biggest companies on the planet (including DuPont, 3M, Johnson Matthey and Imerys), finally reveals the secret of how you can unlock the potential in your business to grow. In the follow up to his book Transforming Technology into Profit, Andy takes you on a journey that explains how the organisation and culture within your business impact your company's ability to innovate. Using his "Three Tiers of Successful Innovation", Andy reveals how to clearly identify what aspects of your business are holding back growth and how to use that information to transform your business into one that facilitates growth by revitalising the structure and culture of your business to focus employee behaviours on adding profitable new revenue streams. Part sequel and part companion volume to his previous book, Andy finally "cracks the code" on how to unleash your business' ability to create and successfully commercialise new products. Written in the author's trademark conversational style, Cracking the Innovation Code offers a refreshingly practical and real-world view, written by someone who has been there and done it, and enhanced by valuable case studies and contributions from numerous senior executives who have made life-long careers out of leading innovation, and with a passion for leading industrial manufacturing businesses.

Silicon Based Unified Memory Devices and Technology - Arup Bhattacharyya 2017-07-06

The primary focus of this book is on basic device concepts, memory cell design, and process technology integration. The first part provides in-depth coverage of conventional nonvolatile memory devices, stack structures from device physics, historical perspectives, and identifies limitations of conventional devices.

The second part reviews advances made in reducing and/or eliminating existing limitations of NVM device parameters from the standpoint of device scalability, application extendibility, and reliability. The final part proposes multiple options of silicon based unified (nonvolatile) memory cell concepts and stack designs (SUMs). The book provides Industrial R&D personnel with the knowledge to drive the future memory technology with the established silicon FET-based establishments of their own. It explores application potentials of memory in areas such as robotics, avionics, health-industry, space vehicles, space sciences, bio-imaging, genetics etc.

Journal of International Commerce & Economics -

Silicon Non-Volatile Memories - Barbara de Salvo 2013-05-10

This book provides a comprehensive overview of the different technological approaches currently being studied to fulfill future memory requirements. Two main research paths are identified and discussed. Different "evolutionary paths" based on new materials and new transistor structures are investigated to extend classical floating gate technology to the 32 nm node. "Disruptive paths" are also covered, addressing 22 nm and smaller IC generations. Finally, the main factors at the origin of these phenomena are identified and analyzed, providing pointers on future research activities and developments in this area.

Building Embedded Systems - Changyi Gu 2016-05-26

Develop the software and hardware you never think about. We're talking about the nitty-gritty behind the buttons on your microwave, inside your thermostat, inside the keyboard used to type this description, and even running the monitor on which you are reading it now. Such stuff is termed embedded systems, and this book shows how to design and develop embedded systems at a professional level. Because yes, many people quietly make a successful career doing just that. Building embedded systems can

be both fun and intimidating. Putting together an embedded system requires skill sets from multiple engineering disciplines, from software and hardware in particular. Building Embedded Systems is a book about helping you do things in the right way from the beginning of your first project: Programmers who know software will learn what they need to know about hardware. Engineers with hardware knowledge likewise will learn about the software side. Whatever your background is, Building Embedded Systems is the perfect book to fill in any knowledge gaps and get you started in a career programming for everyday devices. Author Changyi Gu brings more than fifteen years of experience in working his way up the ladder in the field of embedded systems. He brings knowledge of numerous approaches to embedded systems design, including the System on Programmable Chips (SOPC) approach that is currently growing to dominate the field. His knowledge and experience make Building Embedded Systems an excellent book for anyone wanting to enter the field, or even just to do some embedded programming as a side project. What You Will Learn Program embedded systems at the hardware level Learn current industry practices in firmware development Develop practical knowledge of embedded hardware options Create tight integration between software and hardware Practice a work flow leading to successful outcomes Build from transistor level to the system level Make sound choices between performance and cost Who This Book Is For Embedded-system engineers and intermediate electronics enthusiasts who are seeking tighter integration between software and hardware. Those who favor the System on a Programmable Chip (SOPC) approach will in particular benefit from this book. Students in both Electrical Engineering and Computer Science can also benefit from this book and the real-life industry practice it provides.

Inside Solid State Drives (SSDs) - Rino Micheloni 2018-07-11

The revised second edition of this respected text provides a state-

of-the-art overview of the main topics relating to solid state drives (SSDs), covering NAND flash memories, memory controllers (including booth hardware and software), I/O interfaces (PCIe/SAS/SATA), reliability, error correction codes (BCH and LDPC), encryption, flash signal processing and hybrid storage. Updated throughout to include all recent work in the field, significant changes for the new edition include: A new chapter on flash memory errors and data recovery procedures in SSDs for reliability and lifetime improvement Updated coverage of SSD Architecture and PCI Express Interfaces moving from PCIe Gen3 to PCIe Gen4 and including a section on NVMe over fabric (NVMe) An additional section on 3D flash memories An update on standard reliability procedures for SSDs Expanded coverage of BCH for SSDs, with a specific section on detection A new section on non-binary Low-Density Parity-Check (LDPC) codes, the most recent advancement in the field A description of randomization in the protection of SSD data against attacks, particularly relevant to 3D architectures The SSD market is booming, with many industries placing a huge effort in this space, spending billions of dollars in R&D and product development. Moreover, flash manufacturers are now moving to 3D architectures, thus enabling an even higher level of storage capacity. This book takes the reader through the fundamentals and brings them up to speed with the most recent developments in the field, and is suitable for advanced students, researchers and engineers alike.

Semiconductor Memories and Systems - Andrea Redaelli
2022-06-15

Semiconductor Memories and Systems provides a comprehensive overview of the current state of semiconductor memory at the technology and system levels. After an introduction on market trends and memory applications, the book focuses on mainstream technologies, illustrating their current status, challenges and opportunities, with special attention paid to scalability paths. Technologies discussed include static random access memory

(SRAM), dynamic random access memory (DRAM), non-volatile memory (NVM), and NAND flash memory. Embedded memory and requirements and system level needs for storage class memory are also addressed. Each chapter covers physical operating mechanisms, fabrication technologies, and the main challenges to scalability. Finally, the work reviews the emerging trends for storage class memory, mainly focusing on the advantages and opportunities of phase change based memory technologies. Features contributions from experts from leading companies in semiconductor memory Discusses physical operating mechanisms, fabrication technologies and paths to scalability for current and emerging semiconductor memories Reviews primary memory technologies, including SRAM, DRAM, NVM and NAND flash memory Includes emerging storage class memory technologies such as phase change memory

Solid-State-Drives (SSDs) Modeling - Rino Micheloni 2017-03-28
This book introduces simulation tools and strategies for complex systems of solid-state-drives (SSDs) which consist of a flash multi-core microcontroller plus NAND flash memories. It provides a broad overview of the most popular simulation tools, with special focus on open source solutions. VSSIM, NANDFlashSim and DiskSim are benchmarked against performances of real SSDs under different traffic workloads. PROs and CONs of each simulator are analyzed, and it is clearly indicated which kind of answers each of them can give and at a what price. It is explained, that speed and precision do not go hand in hand, and it is important to understand when to simulate what, and with which tool. Being able to simulate SSD's performances is mandatory to meet time-to-market, together with product cost and quality. Over the last few years the authors developed an advanced simulator named "SSDExplorer" which has been used to evaluate multiple phenomena with great accuracy, from QoS (Quality Of Service) to Read Retry, from LDPC Soft Information to power, from Flash aging to FTL. SSD simulators are also

addressed in a broader context in this book, i.e. the analysis of what happens when SSDs are connected to the OS (Operating System) and to the end-user application (for example, a database search). The authors walk the reader through the full simulation flow of a real system-level by combining SSD Explorer with the QEMU virtual platform. The reader will be impressed by the level of know-how and the combination of models that such simulations are asking for.

Supply Chain Management - Ray R. Venkataraman 2022-01-12
Supply Chain Management: Securing a Superior Global Edge takes a holistic, integrated approach to managing supply chains by addressing the critically important areas of globalization, sustainability, and ethics in every chapter. Authors Ray Venkataraman and Ozgun C. Demirag use a wide variety of real-world cases and examples from the manufacturing and service sectors to illustrate innovative supply chain strategies and technologies. With a focus on decision-making and problem-solving, Supply Chain Management provides students with the tools they need to succeed in today's fiercely competitive, interconnected global economy. Included with this text The online resources for your text are available via the password-protected Instructor Resource Site. Learn more.

T Bytes Hybrid Cloud Infrastructure - IT Shades 2020-11-02
This document brings together a set of latest data points and publicly available information relevant for Hybrid Cloud Infrastructure Technology. We are very excited to share this content and believe that readers will benefit from this periodic publication immensely.

Nonvolatile Memory Technologies with Emphasis on Flash - Joe Brewer 2011-09-23

Presented here is an all-inclusive treatment of Flash technology, including Flash memory chips, Flash embedded in logic, binary cell Flash, and multilevel cell Flash. The book begins with a tutorial of elementary concepts to orient readers who are less

familiar with the subject. Next, it covers all aspects and variations of Flash technology at a mature engineering level: basic device structures, principles of operation, related process technologies, circuit design, overall design tradeoffs, device testing, reliability, and applications.

Inside NAND Flash Memories - Rino Micheloni 2010-07-27
Digital photography, MP3, digital video, etc. make extensive use of NAND-based Flash cards as storage media. To realize how much NAND Flash memories pervade every aspect of our life, just imagine how our recent habits would change if the NAND memories suddenly disappeared. To take a picture it would be necessary to find a film (as well as a traditional camera...), disks or even magnetic tapes would be used to record a video or to listen a song, and a cellular phone would return to be a simple mean of communication rather than a multimedia console. The development of NAND Flash memories will not be set down on the mere evolution of personal entertainment systems since a new killer application can trigger a further success: the replacement of Hard Disk Drives (HDDs) with Solid State Drives (SSDs). SSD is made up by a microcontroller and several NANDs. As NAND is the technology driver for IC circuits, Flash designers and technologists have to deal with a lot of challenges. Therefore, SSD (system) developers must understand Flash technology in order to exploit its benefits and countermeasure its weaknesses. Inside NAND Flash Memories is a comprehensive guide of the NAND world: from circuits design (analog and digital) to Flash reliability (including radiation effects), from testing issues to high-performance (DDR) interface, from error correction codes to NAND applications like Flash cards and SSDs.

HWM - 2006-11

Singapore's leading tech magazine gives its readers the power to decide with its informative articles and in-depth reviews.

Flash Memories - Igor Stievano 2011-09-06

Flash memories and memory systems are key resources for the

development of electronic products implementing converging technologies or exploiting solid-state memory disks. This book illustrates state-of-the-art technologies and research studies on Flash memories. Topics in modeling, design, programming, and materials for memories are covered along with real application examples.

Energy-Aware System Design - Chong-Min Kyung 2011-06-17

Power consumption becomes the most important design goal in a wide range of electronic systems. There are two driving forces towards this trend: continuing device scaling and ever increasing demand of higher computing power. First, device scaling continues to satisfy Moore's law via a conventional way of scaling (More Moore) and a new way of exploiting the vertical integration (More than Moore). Second, mobile and IT convergence requires more computing power on the silicon chip than ever. Cell phones are now evolving towards mobile PC. PCs and data centers are becoming commodities in house and a must in industry. Both supply enabled by device scaling and demand triggered by the convergence trend realize more computation on chip (via multi-core, integration of diverse functionalities on mobile SoCs, etc.) and finally more power consumption incurring power-related issues and constraints. Energy-Aware System Design: Algorithms and Architectures provides state-of-the-art ideas for low power design methods from circuit, architecture to software level and offers design case studies in three fast growing areas of mobile storage, biomedical and security. Important topics and features: - Describes very recent advanced issues and methods for energy-aware design at each design level from circuit and architecture to algorithm level, and also covering important blocks including low power main memory subsystem and on-chip network at architecture level - Explains efficient power conversion and delivery which is becoming important as heterogeneous power sources are adopted for digital and non-digital parts - Investigates 3D die stacking emphasizing temperature awareness for better

perspective on energy efficiency - Presents three practical energy-aware design case studies; novel storage device (e.g., solid state disk), biomedical electronics (e.g., cochlear and retina implants), and wireless surveillance camera systems. Researchers and engineers in the field of hardware and software design will find this book an excellent starting point to catch up with the state-of-the-art ideas of low power design.

Embedded Computing Systems: Applications, Optimization, and Advanced Design - Khalgui, Mohamed 2013-04-30

Embedded computing systems play an important and complex role in the functionality of electronic devices. With our daily routines becoming more reliant on electronics for personal and professional use, the understanding of these computing systems is crucial. Embedded Computing Systems: Applications, Optimization, and Advanced Design brings together theoretical and technical concepts of intelligent embedded control systems and their use in hardware and software architectures. By highlighting formal modeling, execution models, and optimal implementations, this reference source is essential for experts, researchers, and technical supporters in the industry and academia.

Certain NOR and NAND Flash Memory Devices and Products Containing the Same, Inv. 337-TA-560 -

Software Technologies for Embedded and Ubiquitous Systems - Sunggu Lee 2009-11-03

The 7th IFIP Workshop on Software Technologies for Future Embedded and Ubiquitous Systems (SEUS) followed on the success of six previous editions in Capri, Italy (2008), Santorini, Greece (2007), Gyeongju, Korea (2006), Seattle, USA (2005), Vienna, Austria (2004), and Hokodate, Japan (2003), establishing SEUS as one of the emerging workshops in the field of embedded and ubiquitous systems. SEUS 2009 continued the tradition of fostering cross-community scientific excellence and establishing

strong links between research and industry. The fields of both embedded computing and ubiquitous systems have seen considerable growth over the past few years. Given the advances in these fields, and also those in the areas of distributed computing, sensor networks, middleware, etc., the area of ubiquitous embedded computing is now being envisioned as the way of the future. The systems and technologies that will arise in support of ubiquitous embedded computing will undoubtedly need to address a variety of issues, including dependability, real-time, human-computer interaction, autonomy, resource constraints, etc. All of these requirements pose a challenge to the research community. The purpose of SEUS 2009 was to bring together researchers and practitioners with an interest in advancing the state of the art and the state of practice in this emerging field, with the hope of fostering new ideas, collaborations and technologies. SEUS 2009 would not have been possible without the effort of many people.

The Fundamentals of Business Writing: - Claudine L. Boros

2012-04-02

to follow

3D Flash Memories - Rino Micheloni 2016-05-26

This book walks the reader through the next step in the evolution of NAND flash memory technology, namely the development of 3D flash memories, in which multiple layers of memory cells are grown within the same piece of silicon. It describes their working principles, device architectures, fabrication techniques and practical implementations, and highlights why 3D flash is a brand new technology. After reviewing market trends for both NAND and solid state drives (SSDs), the book digs into the details of the flash memory cell itself, covering both floating gate and emerging charge trap technologies. There is a plethora of different materials and vertical integration schemes out there. New memory cells, new materials, new architectures (3D Stacked, BiCS and P-BiCS, 3D FG, 3D VG, 3D advanced architectures);

basically, each NAND manufacturer has its own solution. Chapter 3 to chapter 7 offer a broad overview of how 3D can materialize. The 3D wave is impacting emerging memories as well and chapter 8 covers 3D RRAM (resistive RAM) crosspoint arrays. Visualizing 3D structures can be a challenge for the human brain: this is why all these chapters contain a lot of bird's-eye views and cross sections along the 3 axes. The second part of the book is devoted to other important aspects, such as advanced packaging technology (i.e. TSV in chapter 9) and error correction codes, which have been leveraged to improve flash reliability for decades. Chapter 10 describes the evolution from legacy BCH to the most recent LDPC codes, while chapter 11 deals with some of the most recent advancements in the ECC field. Last but not least, chapter 12 looks at 3D flash memories from a system perspective. Is 14nm the last step for planar cells? Can 100 layers be integrated within the same piece of silicon? Is 4 bit/cell possible with 3D? Will 3D be reliable enough for enterprise and datacenter applications? These are some of the questions that this book helps answering by providing insights into 3D flash memory design, process technology and applications.

Scaled Planar Floating-gate NAND Flash Memory Technology - Shyam Sunder Raghunathan 2010

NAND flash memories are ubiquitous in their use as portable storage media in cellphones, cameras, music players, and other portable electronic devices. The NAND flash memory device, consisting of a floating-gate transistor cell, is the most aggressively scaled electronic device, as evidenced by ever-increasing memory capacities. In this work, we will examine possible problems arising from continued scaling of these structures, and discuss novel solutions to overcome them. Firstly, we investigate scaling of the conventional poly-silicon floating-gate, aimed at reducing cell-to-cell interference. We experimentally delineate a new reliability concern for the first time, with programming current through ultra-thin poly-silicon

floating-gates becoming increasingly ballistic. We also experimentally demonstrate doping-related issues in the polysilicon floating-gate. We then apply a novel metal-based floating-gate cell for the first time, designed to overcome the problems discussed above. We explore factors that influence the choice of metal, and demonstrate excellent functionality in ultra-thin metal floating-gate cells scaled down to 3 nm TiN floating-gate thickness, thus greatly reducing cell-to-cell interference. Finally, in order to facilitate continued scaling of the control dielectric, we explore replacement of the conventional silicon oxide-nitride dielectric with high-k dielectric materials. We integrate polysilicon and metal floating-gate cells with Al₂O₃ high-k control dielectric. Further, we establish that a deeper work-function control gate is helpful in reducing gate-injection. Combining ultra-thin metal floating-gate, high-k control dielectric and deep work-function control gate, we enable the planar floating-gate cell as a scalable candidate.

Nanoelectronics: Physics, Materials and Devices - Angsuman Sarkar 2023-01-17

Nanoelectronics: Physics, Materials and Devices addresses the concepts involved in the exploration of research on nanoscale electronics and photonic devices and their application in next-generation integrated circuits (ICs). The book presents a detailed discussion on the field of nanoscale electronic and photonic devices, as well as the most recent techniques for the modeling and simulation of these devices. It provides an in-depth analysis of theoretical frameworks, the fundamental physics underlying device operation, computational modeling, simulation methods, and circuit applications of nanoscale devices. The purpose of this book is to provide a desirable balance between basic background and concepts to improve device performance. In this book, both qualitative and quantitative approaches are considered to analyze and explore the contributions made by various researchers actively engaged in nanoscale device research. The book's main

motivation is to help solve the challenges of analyzing and exploring the electrical behaviors of contemporary nanoscale device technologies. It purposefully builds the principles of nanoscale electronic devices gradually, invigorating those of micro electronic devices. Addresses the conceptual, architectural, and design challenges faced by emerging nanoscale devices as a replacement of conventional MOSFET. Serves as a guide to researchers by suggesting research directions and potential applications. Explains the use of Technology Computer-Aided Design software (TCAD) to produce numerical simulations of nanoscale devices.

Nanoscale Semiconductor Memories - Santosh K. Kurinec
2017-07-28

Nanoscale memories are used everywhere. From your iPhone to a supercomputer, every electronic device contains at least one such type. With coverage of current and prototypical technologies, *Nanoscale Semiconductor Memories: Technology and Applications* presents the latest research in the field of nanoscale memories technology in one place. It also covers a myriad of applications that nanoscale memories technology has enabled. The book begins with coverage of SRAM, addressing the design challenges as the technology scales, then provides design strategies to mitigate radiation induced upsets in SRAM. It discusses the current state-of-the-art DRAM technology and the need to develop high performance sense amplifier circuitry. The text then covers the novel concept of capacitorless 1T DRAM, termed as Advanced-RAM or A-RAM, and presents a discussion on quantum dot (QD) based flash memory. Building on this foundation, the coverage turns to STT-RAM, emphasizing scalable embedded STT-RAM, and the physics and engineering of magnetic domain wall "racetrack" memory. The book also discusses state-of-the-art modeling applied to phase change memory devices and includes an extensive review of RRAM, highlighting the physics of operation and analyzing different

materials systems currently under investigation. The hunt is still on for universal memory that fits all the requirements of an "ideal memory" capable of high-density storage, low-power operation, unparalleled speed, high endurance, and low cost. Taking an

interdisciplinary approach, this book bridges technological and application issues to provide the groundwork for developing custom designed memory systems.

Strategic Management - Dyer 2023-01-24